Baofeng\_BF\_F8HP

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **#** | **Frequency** | **Name** | **Tone Mode** | **Tone** | **Tone Squelch** | **DTCS** | **RX DTCS** | **DTCS****Polarity** | **Cross mode** | **Duplex** | **Offset** | **Mode** | **Skip** | **Power** | **Comment** |
| 0 | 461.700000 | CAMP W | Tone | 127.3 |  |  |  |  |  | + | 5.000000 | FM |  | Med |  |
| 1 | 461.700000 | CAMP TA |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 2 | 461.700000 | CAMP N | Tone | 127.3 |  |  |  |  |  | + | 5.000000 | FM |  | Med |  |
| 3 | 152.420000 | WCHC R | DTCS |  |  | 271 |  | NN |  | + | 5.260000 | FM |  | Med |  |
| 4 | 152.420000 | WC TA | DTCS |  |  | 271 |  | NN |  |  |  | FM |  | Med |  |
| 5 | 453.125000 |  |  |  |  |  |  |  |  | + | 7.325000 | FM |  | High |  |
| 6 | 460.450000 |  |  |  |  |  |  |  |  |  |  | FM |  | High |  |
| 7 | 461.700000 | CAMP W | Tone | 127.3 |  |  |  |  |  | + | 5.000000 | FM |  | High |  |
| 10 | 467.562500 | SIMPX U |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 11 | 146.520000 | SIMPX V |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 12 | 156.450000 | MAR 09 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 13 | 156.650000 | MAR 13 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 14 | 156.800000 | MAR 16 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 15 | 157.100000 | MAR 22A |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 16 | 156.425000 | MAR 68 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 17 | 156.475000 | MAR 69 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 18 | 156.575000 | MAR 71 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 19 | 156.625000 | MAR 72 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 20 | 162.400000 | NOA TLH |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 21 | 162.500000 | NOA E.P |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 22 | 462.600000 | BG CRK4 | DTCS |  |  | 245 |  | NN |  | + | 5.000000 | FM |  | Med |  |
| 23 | 151.820000 | MURS 1 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 24 | 151.880000 | MURS 2 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 25 | 151.940000 | MURS 3 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 26 | 154.570000 | BLUE DO |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 27 | 154.600000 | GREEN D |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 28 | 462.550000 | GMRS1 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 29 | 462.575000 | GMRS2 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 30 | 462.600000 | GMRS3 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 31 | 462.625000 | GMRS4 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 32 | 462.650000 | GMRS5 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 33 | 462.675000 | GMRS6 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 34 | 462.700000 | GMRS7 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 35 | 462.725000 | GMRS8 |  |  |  |  |  |  |  |  |  | FM |  | Med |  |
| 36 | 462.562500 | FRS1 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |

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| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **#** | **Frequency** | **Name** | **Tone Mode** | **Tone** | **Tone Squelch** | **DTCS** | **RX DTCS** | **DTCS****Polarity** | **Cross mode** | **Duplex** | **Offset** | **Mode** | **Skip** | **Power** | **Comment** |
| 37 | 462.587500 | FRS2 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 38 | 462.612500 | FRS3 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 39 | 462.637500 | FRS4 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 40 | 462.662500 | FRS5 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 41 | 462.687500 | FRS6 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 42 | 462.712500 | FRS7 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 43 | 467.562500 | FRS8 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 44 | 467.587500 | FRS9 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 45 | 467.612500 | FRS10 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 46 | 467.637500 | FRS11 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 47 | 467.662500 | FRS12 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 48 | 467.687500 | FRS13 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 49 | 467.712500 | FRS14 |  |  |  |  |  |  |  |  |  | NFM |  | Med |  |
| 127 | 470.625000 |  |  |  |  |  |  |  |  |  |  | FM |  | High |  |

BTECH\_GRMS-50V2

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **#** | **Frequency** | **Name** | **Tone Mode** | **Tone** | **Tone Squelch** | **DTCS** | **RX DTCS** | **DTCS****Polarity** | **Cross mode** | **Duplex** | **Offset** | **Mode** | **Skip** | **Power** | **Comment** |
| 0 | 461.700000 | CAMP W | Tone | 127.3 |  |  |  |  |  | off |  | FM |  | Low |  |
| 1 | 461.700000 | CAMP TA |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 2 | 461.700000 | CAMP N | Tone | 127.3 |  |  |  |  |  | off |  | FM |  | Low |  |
| 3 | 152.420000 | WCHC R | DTCS |  |  | 271 |  | NN |  | off |  | FM |  | Low |  |
| 4 | 152.420000 | WC TA | DTCS |  |  | 271 |  | NN |  | off |  | FM |  | Low |  |
| 5 | 453.125000 |  |  |  |  |  |  |  |  | off |  | FM |  | Mid |  |
| 6 | 460.450000 |  |  |  |  |  |  |  |  | off |  | FM |  | Mid |  |
| 7 | 461.700000 | CAMP W | Tone | 127.3 |  |  |  |  |  | off |  | FM |  | Mid |  |
| 10 | 467.562500 | SIMPX U |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 11 | 146.520000 | SIMPX V |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 12 | 156.450000 | MAR 09 |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 13 | 156.650000 | MAR 13 |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 14 | 156.800000 | MAR 16 |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 15 | 157.100000 | MAR 22A |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 16 | 156.425000 | MAR 68 |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 17 | 156.475000 | MAR 69 |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 18 | 156.575000 | MAR 71 |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 19 | 156.625000 | MAR 72 |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 20 | 162.400000 | NOA TLH |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 21 | 162.500000 | NOA E.P |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 22 | 462.600000 | BG CRK4 | DTCS |  |  | 245 |  | NN |  | + | 5.000000 | FM |  | Low |  |
| 23 | 151.820000 | MURS 1 |  |  |  |  |  |  |  | off |  | NFM |  | Low |  |
| 24 | 151.880000 | MURS 2 |  |  |  |  |  |  |  | off |  | NFM |  | Low |  |
| 25 | 151.940000 | MURS 3 |  |  |  |  |  |  |  | off |  | NFM |  | Low |  |
| 26 | 154.570000 | BLUE DO |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 27 | 154.600000 | GREEN D |  |  |  |  |  |  |  | off |  | FM |  | Low |  |
| 28 | 462.550000 | GMRS1 |  |  |  |  |  |  |  |  |  | FM |  | Low |  |
| 29 | 462.575000 | GMRS2 |  |  |  |  |  |  |  |  |  | FM |  | Low |  |
| 30 | 462.600000 | GMRS3 |  |  |  |  |  |  |  |  |  | FM |  | Low |  |
| 31 | 462.625000 | GMRS4 |  |  |  |  |  |  |  |  |  | FM |  | Low |  |
| 32 | 462.650000 | GMRS5 |  |  |  |  |  |  |  |  |  | FM |  | Low |  |
| 33 | 462.675000 | GMRS6 |  |  |  |  |  |  |  |  |  | FM |  | Low |  |
| 34 | 462.700000 | GMRS7 |  |  |  |  |  |  |  |  |  | FM |  | Low |  |
| 35 | 462.725000 | GMRS8 |  |  |  |  |  |  |  |  |  | FM |  | Low |  |
| 36 | 462.562500 | FRS1 |  |  |  |  |  |  |  |  |  | NFM |  | Low |  |

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| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **#** | **Frequency** | **Name** | **Tone Mode** | **Tone** | **Tone Squelch** | **DTCS** | **RX DTCS** | **DTCS****Polarity** | **Cross mode** | **Duplex** | **Offset** | **Mode** | **Skip** | **Power** | **Comment** |
| 37 | 462.587500 | FRS2 |  |  |  |  |  |  |  |  |  | NFM |  | Low |  |
| 38 | 462.612500 | FRS3 |  |  |  |  |  |  |  |  |  | NFM |  | Low |  |
| 39 | 462.637500 | FRS4 |  |  |  |  |  |  |  |  |  | NFM |  | Low |  |
| 40 | 462.662500 | FRS5 |  |  |  |  |  |  |  |  |  | NFM |  | Low |  |
| 41 | 462.687500 | FRS6 |  |  |  |  |  |  |  |  |  | NFM |  | Low |  |
| 42 | 462.712500 | FRS7 |  |  |  |  |  |  |  |  |  | NFM |  | Low |  |
| 43 | 467.562500 | FRS8 |  |  |  |  |  |  |  | off |  | NFM |  | Low |  |
| 44 | 467.587500 | FRS9 |  |  |  |  |  |  |  | off |  | NFM |  | Low |  |
| 45 | 467.612500 | FRS10 |  |  |  |  |  |  |  | off |  | NFM |  | Low |  |
| 46 | 467.637500 | FRS11 |  |  |  |  |  |  |  | off |  | NFM |  | Low |  |
| 47 | 467.662500 | FRS12 |  |  |  |  |  |  |  | off |  | NFM |  | Low |  |
| 48 | 467.687500 | FRS13 |  |  |  |  |  |  |  | off |  | NFM |  | Low |  |
| 49 | 467.712500 | FRS14 |  |  |  |  |  |  |  | off |  | NFM |  | Low |  |
| 127 | 470.625000 |  |  |  |  |  |  |  |  | off |  | FM |  | Mid |  |
| 249 | 162.400000 | NOAA01 |  |  |  |  |  |  |  | off |  | NFM |  | High |  |
| 250 | 162.425000 | NOAA02 |  |  |  |  |  |  |  | off |  | NFM |  | High |  |
| 251 | 162.450000 | NOAA03 |  |  |  |  |  |  |  | off |  | NFM |  | High |  |
| 252 | 162.475000 | NOAA04 |  |  |  |  |  |  |  | off |  | NFM |  | High |  |
| 253 | 162.500000 | NOAA05 |  |  |  |  |  |  |  | off |  | NFM |  | High |  |
| 254 | 162.525000 | NOAA06 |  |  |  |  |  |  |  | off |  | NFM |  | High |  |
| 255 | 162.550000 | NOAA07 |  |  |  |  |  |  |  | off |  | NFM |  | High |  |

[2023‐12‐05 10:03:42,212] chirp.logger ‐ DEBUG: CHIRP next‐20231204 on Win32 (Unknown 10.0:22621) (Python 3.10.8)

[2023‐12‐05 10:03:42,259] chirp.wxui ‐ DEBUG: Using locale: en\_US (276) [2023‐12‐05 10:03:42,259] chirp.wxui ‐ DEBUG: Translation loaded=True for CHIRP: en\_US (de,el,en\_US,es,fr,hu,it,ja\_JP,nl,pl,pt\_BR,ru,tr\_TR,uk\_UA,zh\_CN) from C:\Program Files (x86)\CHIRP\chirp\locale

[2023‐12‐05 10:03:42,259] chirp.wxui ‐ DEBUG: Translation loaded=False for wxstd: en\_US (af,an,ar,ca,ca@valencia,co,cs,da,de,el,es,eu,fa\_IR,fi,fr,gl\_ES,hi,hr,hu,id,it,ja,k a,ko\_KR,lt,lv,ms,nb,ne,nl,pl,pt,pt\_BR,ro,ru,sk,sl,sq,sv,ta,tr,uk,vi,zh\_CN,zh\_TW) [2023‐12‐05 10:03:42,593] main ‐ INFO: Python/3.10.8 // Windows/Windows‐10‐10.0.22621‐SP0 // CHIRP/next‐20231204 // wx/4.2.0 msw (phoenix) wxWidgets 3.2.0

[2023‐12‐05 10:03:42,870] chirp.wxui.main ‐ INFO: Server reports next‐20231204 is latest

[2023‐12‐05 10:03:56,114] chirp.chirp\_common ‐ DEBUG: Loaded metadata: {'vendor': 'Baofeng', 'model': 'BF‐F8HP', 'chirp\_version': 'daily‐20221217', 'rclass': 'BaofengBFF8HPRadio', 'variant': ''}

[2023‐12‐05 10:03:56,114] chirp.chirp\_common ‐ DEBUG: Loaded metadata: {'vendor': 'Baofeng', 'model': 'BF‐F8HP', 'chirp\_version': 'daily‐20221217', 'rclass': 'BaofengBFF8HPRadio', 'variant': ''}

[2023‐12‐05 10:03:56,114] chirp.chirp\_common ‐ DEBUG: Parsed version 'daily‐20221217' to (20221217,)

[2023‐12‐05 10:03:56,114] chirp.chirp\_common ‐ DEBUG: Parsed version 'next‐20231204' to (0,)

[2023‐12‐05 10:03:56,114] chirp.chirp\_common ‐ WARNING: Image is from version daily‐20221217 but we are next‐20231204

[2023‐12‐05 10:04:05,632] chirp.chirp\_common ‐ DEBUG: Loaded metadata:

{'mem\_extra': {}, 'rclass': 'GMRS50V2', 'vendor': 'BTECH', 'model': 'GMRS‐50V2', 'variant': '', 'chirp\_version': 'next‐20230219'}

[2023‐12‐05 10:04:05,632] chirp.chirp\_common ‐ DEBUG: Loaded metadata:

{'mem\_extra': {}, 'rclass': 'GMRS50V2', 'vendor': 'BTECH', 'model': 'GMRS‐50V2', 'variant': '', 'chirp\_version': 'next‐20230219'}

[2023‐12‐05 10:04:05,632] chirp.chirp\_common ‐ DEBUG: Parsed version 'next‐20230219' to (0,)

[2023‐12‐05 10:04:05,632] chirp.chirp\_common ‐ DEBUG: Parsed version 'next‐20231204' to (0,)

[2023‐12‐05 10:04:05,727] chirp.drivers.btech ‐ INFO: Radio ranges: VHF 130000000 to 179000000

[2023‐12‐05 10:04:05,727] chirp.drivers.btech ‐ INFO: Radio ranges: UHF 400000000 to 520000000

[2023‐12‐05 10:04:18,669] chirp.wxui.memedit ‐ DEBUG: CHIRP‐native paste: {'mems': [<Memory 0: freq=461700000,name='CAMP W',vfo=0,rtone=127.3,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='Tone',cross\_mode='Tone‐>T one',dtcs\_polarity='NN',skip='',power=Med (36 dBm),duplex='+',offset=5000000,mode='FM',tuning\_step=5.0,comment='',empty=False,imm utable=[],extra.bcl='False',extra.pttid='Off',extra.scode='1'>, <Memory 1: freq=461700000,name='CAMP TA',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone'

,dtcs\_polarity='NN',skip='',power=Med (36

dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= [],extra.bcl='False',extra.pttid='Off',extra.scode='1'>, <Memory 2: freq=461700000,name='CAMP N',vfo=0,rtone=127.3,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='Tone',cross\_mode='Tone‐>T one',dtcs\_polarity='NN',skip='',power=Med (36 dBm),duplex='+',offset=5000000,mode='FM',tuning\_step=5.0,comment='',empty=False,imm utable=[],extra.bcl='False',extra.pttid='Off',extra.scode='1'>, <Memory 3: freq=152420000,name='WCHC R',vfo=0,rtone=88.5,ctone=88.5,dtcs=271,rx\_dtcs=271,tmode='DTCS',cross\_mode='Tone‐> Tone',dtcs\_polarity='NN',skip='',power=Med (36 dBm),duplex='+',offset=5260000,mode='FM',tuning\_step=5.0,comment='',empty=False,imm utable=[],extra.bcl='False',extra.pttid='Off',extra.scode='1'>, <Memory 4: freq=152420000,name='WC TA',vfo=0,rtone=88.5,ctone=88.5,dtcs=271,rx\_dtcs=271,tmode='DTCS',cross\_mode='Tone‐

>Tone',dtcs\_polarity='NN',skip='',power=Med (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= [],extra.bcl='False',extra.pttid='Off',extra.scode='1'>, <Memory 5: freq=453125000,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cros s\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=High (39 dBm),duplex='+',offset=7325000,mode='FM',tuning\_step=5.0,comment='',empty=False,imm utable=[],extra.bcl='False',extra.pttid='Off',extra.scode='1'>, <Memory 6: freq=460450000,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cros s\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=High (39 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= [],extra.bcl='False',extra.pttid='Off',extra.scode='1'>, <Memory 7: freq=461700000,name='CAMP W',vfo=0,rtone=127.3,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='Tone',cross\_mode='Tone‐>T one',dtcs\_polarity='NN',skip='',power=High (39 dBm),duplex='+',offset=5000000,mode='FM',tuning\_step=5.0,comment='',empty=False,imm utable=[],extra.bcl='False',extra.pttid='Off',extra.scode='1'>, <Memory 8: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 9:

freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 10: freq=467562500,name='SIMPX U',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone', dtcs\_polarity='NN',skip='',power=Med (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= [],extra.bcl='False',extra.pttid='Off',extra.scode='1'>, <Memory 11: freq=146520000,name='SIMPX V',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone', dtcs\_polarity='NN',skip='',power=Med (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= [],extra.bcl='False',extra.pttid='Off',extra.scode='1'>, <Memory 12: freq=156450000,name='MAR 09',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone'

,dtcs\_polarity='NN',skip='',power=Med (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable=

[],extra.bcl='False',extra.pttid='Off',extra.scode='1'>, <Memory 13: freq=156650000,name='MAR 13',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone'

,dtcs\_polarity='NN',skip='',power=Med (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= [],extra.bcl='False',extra.pttid='Off',extra.scode='1'>, <Memory 14: freq=156800000,name='MAR 16',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone'

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,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 74: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

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,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 76: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 77: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 78: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 79: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 80: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 81: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 82: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 83: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 84: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 85: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 86: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 87: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 88: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 89: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 90: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 91: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 92: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 93: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 94: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 95: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='

Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 96: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 97: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 98: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 99: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 100: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 101: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 102: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 103: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 104: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 105: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 106: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 107: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 108: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 109: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 110: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 111: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 112:

freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 113: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 114: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 115: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 116: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 117: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 118: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 119: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 120: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 121: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 122: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 123: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 124: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 125: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 126: freq=0,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode=' Tone‐>Tone',dtcs\_polarity='NN',skip='',power=None,duplex='',offset=600000,mode='FM'

,tuning\_step=5.0,comment='',empty=True,immutable=[]>, <Memory 127: freq=470625000,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cros s\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=High (39 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= [],extra.bcl='False',extra.pttid='Off',extra.scode='1'>], 'features':

<chirp.chirp\_common.RadioFeatures object at 0x000001E169666A70>, 'source\_radio\_id':

'Baofeng\_BF‐F8HP', 'source': ‐31955}

[2023‐12‐05 10:04:18,669] chirp.wxui.memedit ‐ DEBUG: Paste is from identical radio class: False

[2023‐12‐05 10:04:18,669] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 0: freq=461700000,name='CAMP W',vfo=0,rtone=127.3,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='Tone',cross\_mode='Tone‐>T one',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='+',offset=5000000,mode='FM',tuning\_step=5.0,comment='',empty=False,imm utable=[]>

[2023‐12‐05 10:04:18,669] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,669] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,669] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,669] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,669] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex='+'‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ex tra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐

>'Tone/DTCS',immutable=[]‐>['duplex', 'offset'],offset=5000000‐>0

[2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 1: freq=461700000,name='CAMP TA',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone'

,dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 2: freq=461700000,name='CAMP N',vfo=0,rtone=127.3,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='Tone',cross\_mode='Tone‐>T one',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='+',offset=5000000,mode='FM',tuning\_step=5.0,comment='',empty=False,imm utable=[]>

[2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex='+'‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ex tra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐

>'Tone/DTCS',immutable=[]‐>['duplex', 'offset'],offset=5000000‐>0

[2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 3: freq=152420000,name='WCHC R',vfo=0,rtone=88.5,ctone=88.5,dtcs=271,rx\_dtcs=271,tmode='DTCS',cross\_mode='Tone‐> Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='+',offset=5260000,mode='FM',tuning\_step=5.0,comment='',empty=False,imm utable=[]>

[2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex='+'‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ex tra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐

>'Tone/DTCS',immutable=[]‐>['duplex', 'offset'],offset=5260000‐>0,rx\_dtcs=271‐>23 [2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 4: freq=152420000,name='WC TA',vfo=0,rtone=88.5,ctone=88.5,dtcs=271,rx\_dtcs=271,tmode='DTCS',cross\_mode='Tone‐

>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset'],rx\_dtcs=271‐>23

[2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 5: freq=453125000,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cros s\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Mid (40 dBm),duplex='+',offset=7325000,mode='FM',tuning\_step=5.0,comment='',empty=False,imm utable=[]>

[2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex='+'‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ex tra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐

>'Tone/DTCS',immutable=[]‐>['duplex', 'offset'],offset=7325000‐>0

[2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 6: freq=460450000,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cros

s\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Mid (40 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 7: freq=461700000,name='CAMP W',vfo=0,rtone=127.3,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='Tone',cross\_mode='Tone‐>T one',dtcs\_polarity='NN',skip='',power=Mid (40 dBm),duplex='+',offset=5000000,mode='FM',tuning\_step=5.0,comment='',empty=False,imm utable=[]>

[2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex='+'‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ex tra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐

>'Tone/DTCS',immutable=[]‐>['duplex', 'offset'],offset=5000000‐>0

[2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,685] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 10: freq=467562500,name='SIMPX U',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone', dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,685] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 11: freq=146520000,name='SIMPX V',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone',

dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 12: freq=156450000,name='MAR 09',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone'

,dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 13: freq=156650000,name='MAR 13',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone'

,dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 14: freq=156800000,name='MAR 16',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone'

,dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable=

[]>

[2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 15: freq=157100000,name='MAR 22A',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone ',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 16: freq=156425000,name='MAR 68',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone'

,dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 17: freq=156475000,name='MAR 69',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone'

,dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: This mem was empty before

[2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 18: freq=156575000,name='MAR 71',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone'

,dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,701] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 19: freq=156625000,name='MAR 72',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone'

,dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,701] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 20: freq=162400000,name='NOA TLH',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone ',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty

[2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 21: freq=162500000,name='NOA E.P',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone ',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 22: freq=462600000,name='BG CRK4',vfo=0,rtone=88.5,ctone=88.5,dtcs=245,rx\_dtcs=245,tmode='DTCS',cross\_mode='Ton e‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='+',offset=5000000,mode='FM',tuning\_step=5.0,comment='',empty=False,imm utable=[]>

[2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS',rx\_dt cs=245‐>23

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 23: freq=151820000,name='MURS 1',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone', dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 24: freq=151880000,name='MURS 2',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone', dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 25: freq=151940000,name='MURS 3',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone', dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 26: freq=154570000,name='BLUE DO',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone'

,dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set:

duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 27: freq=154600000,name='GREEN D',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cross\_mode='Tone‐>Tone', dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 28: freq=462550000,name='GMRS1',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode=''

,cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,716] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,716] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS' [2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 29: freq=462575000,name='GMRS2',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode=''

,cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS' [2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 30: freq=462600000,name='GMRS3',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode=''

,cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36

dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS' [2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 31: freq=462625000,name='GMRS4',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode=''

,cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS' [2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 32: freq=462650000,name='GMRS5',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode=''

,cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS' [2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 33: freq=462675000,name='GMRS6',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode=''

,cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from

set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS' [2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 34: freq=462700000,name='GMRS7',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode=''

,cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS' [2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 35: freq=462725000,name='GMRS8',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode=''

,cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS' [2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 36: freq=462562500,name='FRS1',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='', cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS',immut able=[]‐>['duplex', 'offset', 'power']

[2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 37: freq=462587500,name='FRS2',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='', cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,732] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,732] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS',immut able=[]‐>['duplex', 'offset', 'power']

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 38: freq=462612500,name='FRS3',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='', cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS',immut able=[]‐>['duplex', 'offset', 'power']

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 39: freq=462637500,name='FRS4',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='', cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS',immut able=[]‐>['duplex', 'offset', 'power']

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 40: freq=462662500,name='FRS5',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='', cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting

default for extras.

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS',immut able=[]‐>['duplex', 'offset', 'power']

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 41: freq=462687500,name='FRS6',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='', cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS',immut able=[]‐>['duplex', 'offset', 'power']

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 42: freq=462712500,name='FRS7',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='', cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',extra.pttid='<missin g>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>'Tone/DTCS',immut able=[]‐>['duplex', 'offset', 'power']

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 43: freq=467562500,name='FRS8',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='', cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐>

'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 44: freq=467587500,name='FRS9',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='', cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 45: freq=467612500,name='FRS10',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode=''

,cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 46: freq=467637500,name='FRS11',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode=''

,cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,748] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,748] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 47: freq=467662500,name='FRS12',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode=''

,cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,764] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,764] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 48: freq=467687500,name='FRS13',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode=''

,cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,764] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,764] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 49: freq=467712500,name='FRS14',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode=''

,cross\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Low (36 dBm),duplex='',offset=0,mode='NFM',tuning\_step=5.0,comment='',empty=False,immutable

=[]>

[2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,764] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before

[2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,764] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,779] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before

[2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,795] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,810] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,810] chirp.drivers.btech ‐ DEBUG: This mem was empty before [2023‐12‐05 10:04:18,810] chirp.wxui.memedit ‐ DEBUG: Setting memory: <Memory 127: freq=470625000,name='',vfo=0,rtone=88.5,ctone=88.5,dtcs=23,rx\_dtcs=23,tmode='',cros s\_mode='Tone‐>Tone',dtcs\_polarity='NN',skip='',power=Mid (40 dBm),duplex='',offset=0,mode='FM',tuning\_step=5.0,comment='',empty=False,immutable= []>

[2023‐12‐05 10:04:18,810] chirp.drivers.btech ‐ DEBUG: This mem was empty before

[2023‐12‐05 10:04:18,810] chirp.drivers.btech ‐ DEBUG: Zeroing new memory [2023‐12‐05 10:04:18,810] chirp.drivers.btech ‐ DEBUG: New mem is NOT empty [2023‐12‐05 10:04:18,810] chirp.drivers.btech ‐ DEBUG: old mem was empty. Setting default for extras.

[2023‐12‐05 10:04:18,810] chirp.wxui.memedit ‐ DEBUG: Driver refresh delta from set: duplex=''‐>'off',extra.bcl='<missing>'‐>'False',extra.optsig='<missing>'‐>'OFF',ext ra.pttid='<missing>'‐>'OFF',extra.scode='<missing>'‐>'1',extra.spmute='<missing>'‐> 'Tone/DTCS',immutable=[]‐>['duplex', 'offset']

[2023‐12‐05 10:04:41,391] chirp.wxui.common ‐ DEBUG: Revealing path C:\Users\dbell\AppData\Local\Temp\chirp\_debug‐fgdn7pr2.txt

[2023‐12‐05 10:09:43,513] chirp.wxui.memedit ‐ DEBUG: Ignoring drag from row 8 ‐> 8 [2023‐12‐05 10:09:43,513] chirp.wxui.memedit ‐ DEBUG: Target rejected our memories